

On the feasibility of overnight industrial high-fidelity simulations of CSP technologies on modern HPC systems

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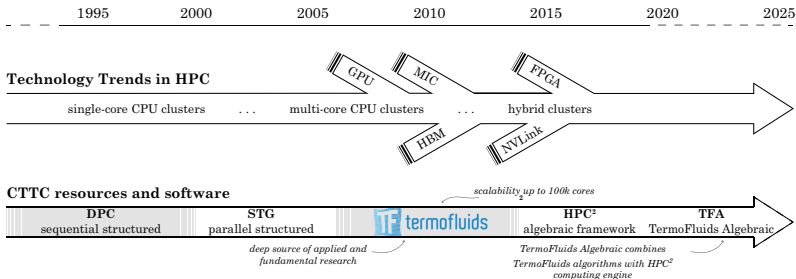
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 - Performance portability
- 2 Algebra-based design
 - HPC²
 - TFA
- 3 Addressing the challenges
 - Numerical challenges
 - Computational challenges
- 4 Numerical results
 - Exploiting symmetries
 - TFA vs OpenFOAM
 - Towards overnight LES
- 5 Concluding remarks

Motivation

Changing landscape...

Rank	System	Cores	Rmax (PFlop/s)	Rpeak (PFlop/s)	Power (kW)
1	Frontier - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE DOE/SC/Oak Ridge National Laboratory United States	8,699,904	1,194.00	1,679.82	22,703
2	Supercomputer Fugaku - Supercomputer Fugaku, A64FX 48C 2.2GHz, Tofu interconnect D, Fujitsu RIKEN Center for Computational Science Japan	7,630,848	442.01	537.21	29,899
3	LUMI - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE EuroHPC/CSC Finland	2,220,288	309.10	428.70	6,016
4	Leonardo - BullSequana XH2000, Xeon Platinum 8358 32C 2.6GHz, NVIDIA A100 SXM4 64 GB, Quad-rail NVIDIA HDR100 Infiniband, Atos EuroHPC/CINECA Italy	1,824,768	238.70	304.47	7,404

Changing landscape, changing codes



Towards performance portability

Stencil-based design

Looping across the mesh performing local operations

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- **Pros:** More flexible and compute-intensive, lower memory requirements

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Express discrete operators as sparse matrices and fields as vectors

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- **Pros:** All operations reduce to 3 linear algebra kernels generally available in standard libraries (eg, Intel MKL, cuSPARSE, cISPARSE)

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Express discrete operators as sparse matrices and fields as vectors

- **Pros:** All operations reduce to 3 linear algebra kernels generally available in standard libraries (eg, Intel MKL, cuSPARSE, cISPARSE)
- **Cons:** Less compute-intensive, higher memory requirements, requires algorithmic reformulation

Algebra-based design

HPC² library

HPC² library

- Sparse linear algebra code
- Modular design ensuring natural portability

X. Álvarez-Farré et al. (2018). “HPC² – A fully-portable, algebra-based framework for heterogeneous computing. Application to CFD” in *Computers & Fluids*.

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HPC² library

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- Sparse linear algebra code
- Modular design ensuring natural portability
- In C++ and currently supporting MPI+OpenMP, CUDA and OpenCL
- Implements a few highly optimized kernels. Namely:
 - Matrix-vector product
 - Linear combination of vectors
 - Dot product of vectors

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- Als includes specialized kernels and Poisson solvers

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- Incompressible CFD simulation code
- Fully-conservative discretisation for collocated unstructured grids

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- Fully-conservative discretisation for collocated unstructured grids
- Algebra-based, formulated in terms of:
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Addressing the challenges

Algorithmic reformulation

Some extra effort is required to reformulate algebraically certain operations applying “locally”.

Recently, it was shown how to effectively implement flux limiters and CFL-like time-steps.

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Algebra-based boundary conditions

Virtually all boundary conditions can be expressed as an affine transformation:

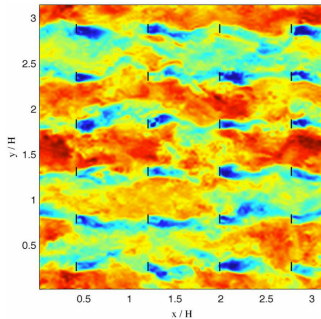
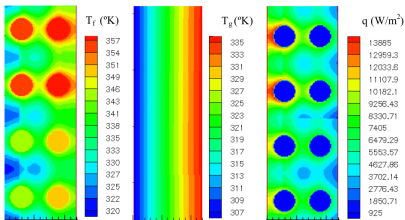
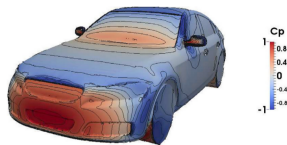
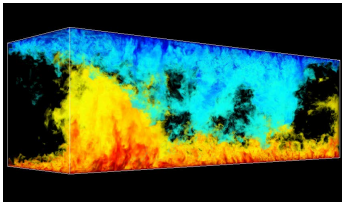
$$\psi_h \rightarrow A\psi_h + b_h,$$

where fluxes are imposed through A and values through b_h

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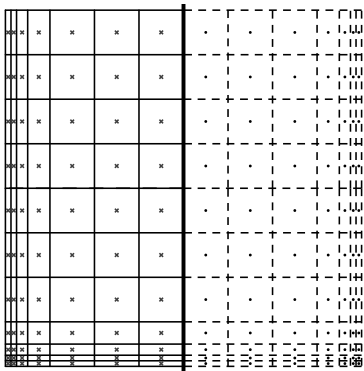
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New opportunities: exploiting regular geometries – 1

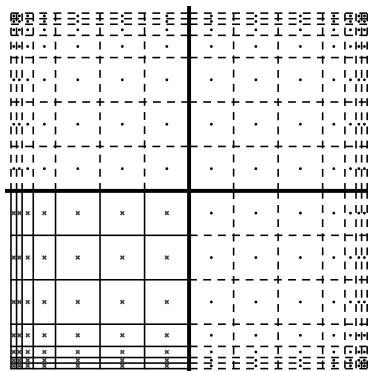


F. Dabbagh et al. (2017) in *Physics of Fluids*
 D.E. Aljure et al. (2018) in *Journal of Wind Engineering and Industrial Aerodynamics*
 L. Paniagua et al. (2014) in *Numerical Heat Transfer, Part B: Fundamentals*
 M. Calaf et al. (2010) in *Physics of Fluids*

New opportunities: exploiting regular geometries – 2



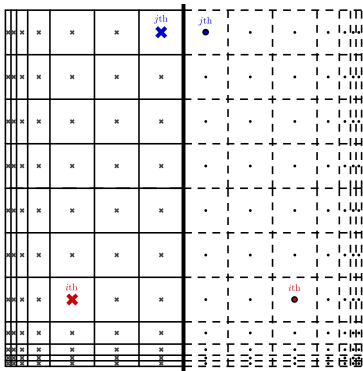
(a) 1 symmetry



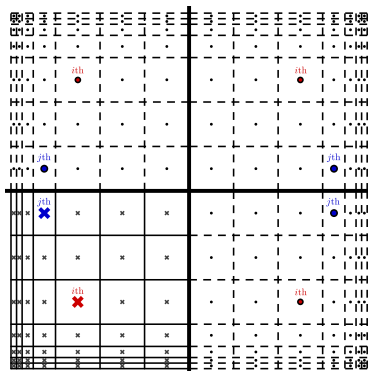
(b) 2 symmetries

Figure: 2D meshes with varying number of symmetries.

New opportunities: exploiting regular geometries – 3



(a) 1 symmetry



(b) 2 symmetries

Figure: “Mirrored” ordering on 2D meshes with a varying no. of symmetries.

New opportunities: exploiting regular geometries – 4

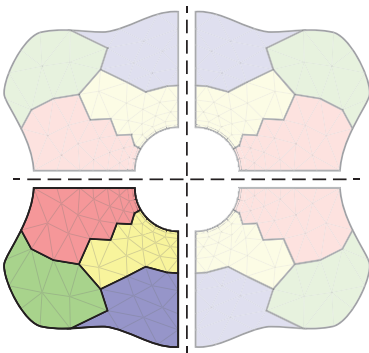


Figure: “Mirrored” partitioning on an unstructured 2D meshes with 2 symmetries.

Computational advantages

On a domain with n_b repeated/mirrored subdomains, virtually all operators satisfy (or a compatible expression):

$$\bar{H} = \mathbb{I}_{n_b} \otimes H, \quad (1)$$

where $\bar{H} \in \mathbb{R}^{n \times n}$ stands for the operator itself and $H \in \mathbb{R}^{n/n_b \times n/n_b}$ for its restriction to the base mesh.

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$$y = \begin{pmatrix} H & & \\ & \ddots & \\ & & H \end{pmatrix} \begin{pmatrix} x_1 \\ \vdots \\ x_{n_b} \end{pmatrix} \in \mathbb{R}^n, \quad (2)$$

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can be replaced with an SpMM on H :

$$(y_1 \dots y_{n_b}) = H(x_1 \dots x_{n_b}) \in \mathbb{R}^{n/n_b \times n_b}. \quad (3)$$

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SpMV vs SpMM

- SpMM reads H n_b less times
- \bar{H} takes n_b times more memory than H

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Numerical results

Exploiting symmetries – 1

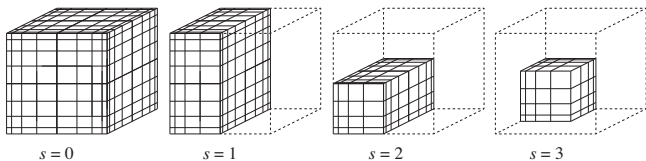
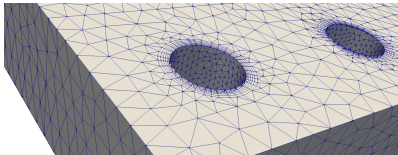
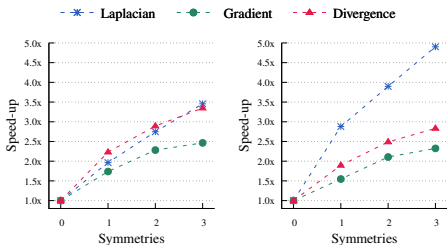
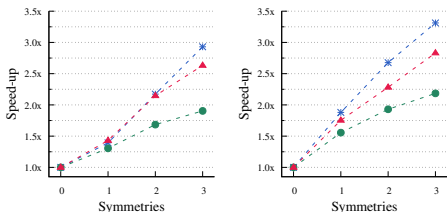


Figure: Top: 17.7M wall-bounded pin matrix heat exchanger. Bottom: 15.5M cubic mesh.

Exploiting symmetries – 2



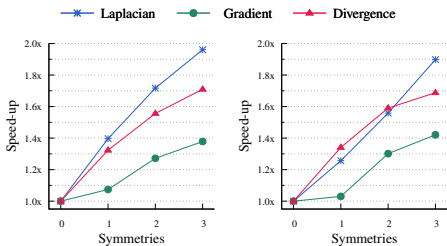
(a) 2x Intel Xeon 8160



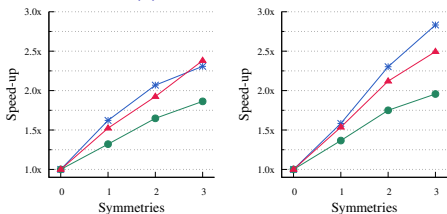
(b) NVIDIA RTX A5000

Figure: SpMM speedups on a fixed problem size. Left: structured. Right: unstructured.

Exploiting symmetries – 3



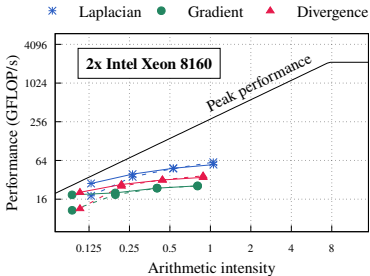
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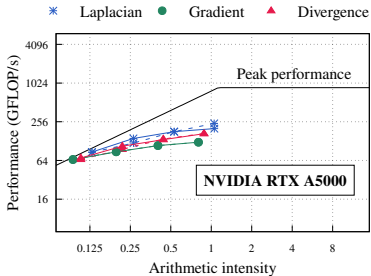
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Figure: SpMM speedups on a fixed base mesh. Left: structured. Right: unstructured.

Exploiting symmetries – 4



(a) 2x Intel Xeon 8160



(b) NVIDIA RTX A5000

Figure: SpMM's roofline analysis. Dashed: fixed problem size. Solid: fixed base mesh.

Exploiting symmetries – 5

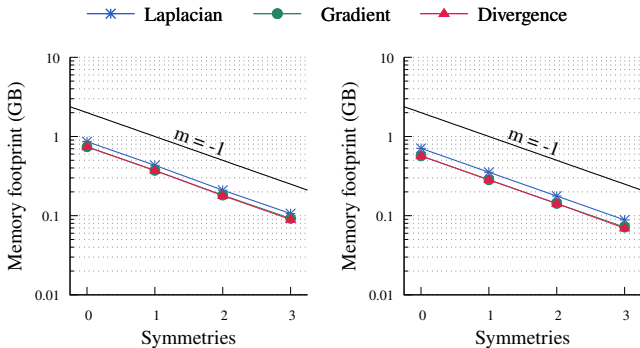


Figure: Operators' memory footprint. Left: structured. Right: unstructured.

Exploiting symmetries – 5

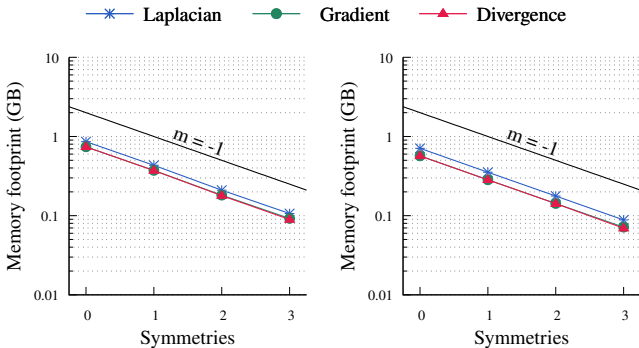
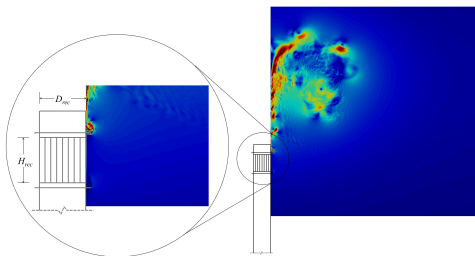


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More generally: repeated geometries lead to n_b times smaller footprints!

Test-case: CSP central tower receiver



Assumptions for industrial LES

LES limitation to be routinely applied in the industry: to be completed overnight.

- Mesh resolution: 300M-500M grid
- Simulated time period: 150 time units
- Wall-clock time limit: 16 hours

TFA vs OpenFOAM: strong scalability

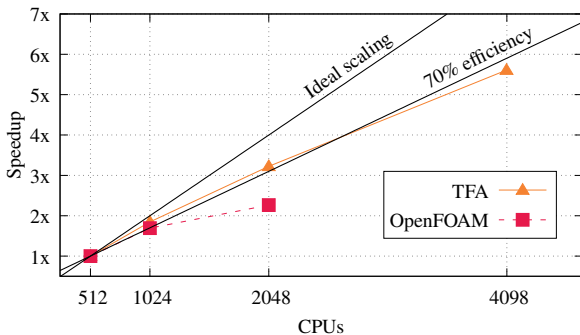


Figure: Scalability of TFA (MPI+OpenMP) vs OpenFOAM (MPI-only) down to 70% efficiency on a 500M CSP structured grid. Ran on AMD EPYC Rome nodes.

Towards overnight LES

Assuming constant Δt , to simulate τ time units, the required time-steps are:

$$n_{\Delta t} = \frac{\tau}{\Delta t}.$$

F.X. Trias et al. (2010). "Direct numerical simulation of a differentially heated cavity of aspect ratio 4 with Rayleigh numbers up to 10^{11} – Part I: Numerical methods and time-averaged flow" in *International Journal of Heat and Mass Transfer*.

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Recalling that LES are generally convection-dominated, for some correction constant c :

$$\Delta t = \min \left\{ \frac{\Delta x_i}{|u_i|} \right\} \simeq \frac{c}{\sqrt[3]{N}},$$

where Δx , u and N stand for the cell length, local velocity and mesh size.

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$$T_{\text{LES}}(N) \simeq n_{\Delta t} \frac{T_{\Delta t}^{\text{eff}} N}{N_{\text{ref}}} = \frac{\tau T_{\Delta t}^{\text{eff}}}{c N_{\text{ref}}} \sqrt[3]{N^4}.$$

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According to Trias et al. (2010), $c \simeq 0.3$ and after 100 time units the flow starts becoming statistically stationary, so we take $\tau = 150$.

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TFA vs OpenFOAM: strong scalability

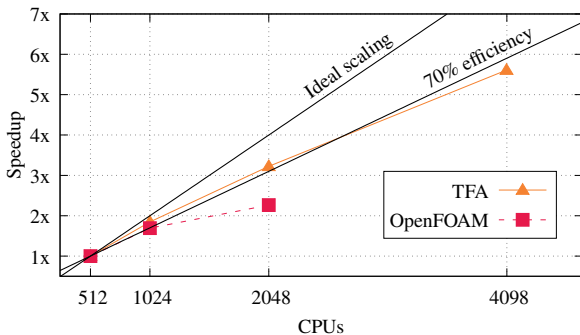


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Towards overnight LES – 95% parallel efficiency

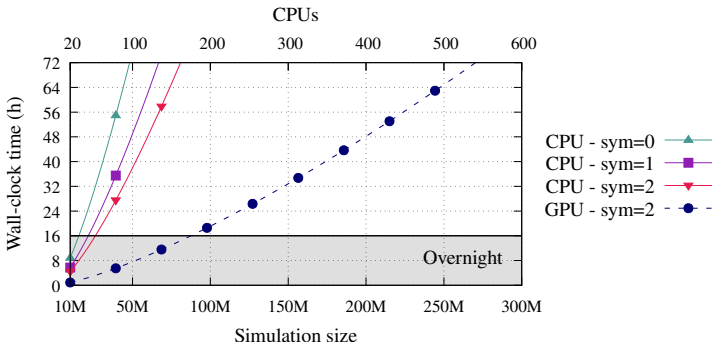


Figure: Estimated largest affordable overnight simulations on a 500M CSP structured grid. Ran on AMD EPYC Rome nodes and assuming a conservative 5x GPU speedup.

Towards overnight LES – 75% parallel efficiency

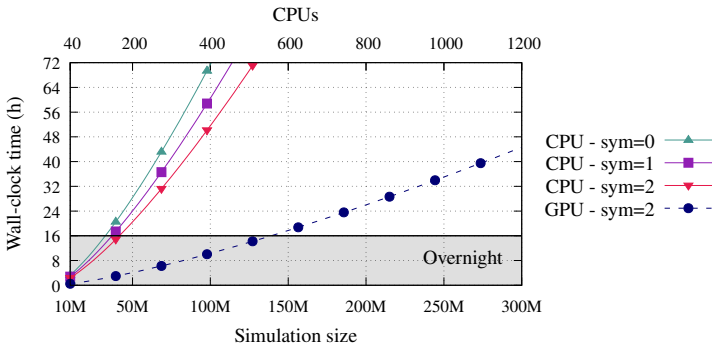


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Towards overnight LES – 65% parallel efficiency

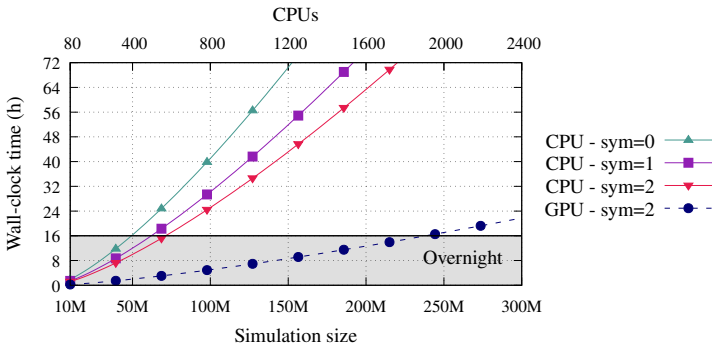


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Concluding remarks

Conclusions

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- The algebra-based design allows for easy performance portability

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- Despite the challenges it poses, it opens the door to new opportunities:
 - Specialised kernels such as SpMM
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- Extend TFA vs OpenFOAM comparison

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- Evaluate the gains on regular domains like wind-farms and heat exchangers

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- Evaluate the gains with GPUs
- Evaluate the gains on regular domains like wind-farms and heat exchangers
- Evaluate the gains with a novel multigrid reduction framework

Thanks for your attention!